Advanced Systems Lab

Spring 2020 Lecture: DSL-based program generation for performance (Spiral)

Instructor: Markus Püschel, Ce Zhang TA: Joao Rivera, Bojan Karlas, several more

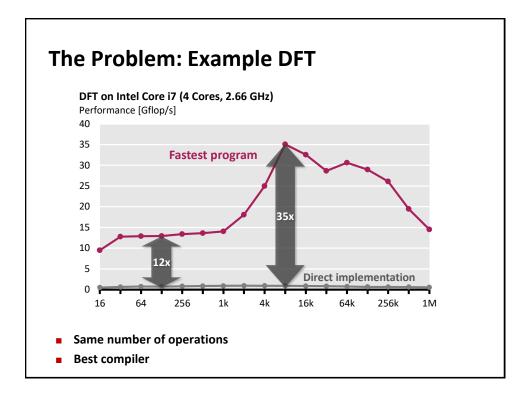
EITH Eidgenössische Technische Hochschule Zürich Swiss Federal Institute of Technology Zurich

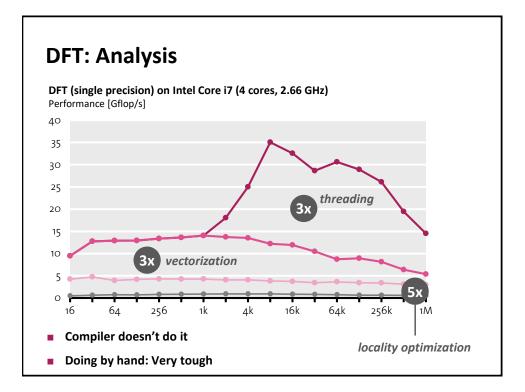
Spiral: DSL-Based Program Generation for Performance

- www.spiral.net
- Franz Franchetti, Tze-Meng Low, Thom Popovici, Richard Veras, Daniele G. Spampinato, Jeremy Johnson, P, James C. Hoe and José M. F. Moura
 SPIRAL: Extreme Performance Portability Proceedings of the IEEE, special issue on ``From High Level Specification to High Performance Code", Vol. 106, No. 11, 2018
- P, Franz Franchetti and Yevgen Voronenko <u>Spiral</u> in Encyclopedia of Parallel Computing, Eds. David Padua, pp. 1920-1933, Springer 2011
- P, José M. F. Moura, Jeremy Johnson, David Padua, Manuela Veloso, Bryan Singer, Jianxin Xiong, Franz Franchetti, Aca Gacic, Yevgen Voronenko, Kang Chen, Robert W. Johnson and Nicholas Rizzolo,
 <u>SPIRAL: Code Generation for DSP Transforms</u> Proceedings of the IEEE, special issue on «Program Generation, Optimization, and

Adaptation", Vol. 93, No. 2, pp. 232- 275, 2005

2





Our Goal:

Computer writes high performance library code

Generate Code رأس) "click"

				_		
Select convolutional code						
Select a preset code or customize parame	suers					
© custom	rate 1/2	co	de rate (?)			
Voyager						
NASA-DSN	К 7		nstraint length (?)			
CCSDS/NASA-GSFC	polynomials 109		olynomials for the de in decimal notation			
 WiMax 	79	(?		1		
CDMA IS-95A			-			
 LTE (3GPP - Long Term Evolution) 						
UWB (802.15)						
CDMA 2000						
 Cassini 						
 Mars Pathfinder & Stereo 						DFT IP Cores
Select implementation options						BITH COICS
frame length	2048	unpadded frame lei	ngt parameter	value	range	explanation
Vectorization level	scalar C 🔹	type of code (?)	Problem specificat	tion	_	<u>.</u>
Generate Code Reset						
			transform size	64 🔹	4-32768	Number of samples (?)
Viterbi Decoder	•		direction	forward 💌		forward or inverse DFT (?)
			data type	fixed point		fixed or floating point (?)
				16 bits	4-32 bits	fixed point precision (?)
				unscaled 💌		scaling mode (?)
			Parameters contro	olling implementation		
			architecture	fully streaming 💌		iterative or fully streaming (?)
			radix	2 💌	2, 4, 8, 16, 32, 64	size of DFT basic block (?)
			streaming width	2 💌	2-64	number of complex words per cycle (?)
			data ordering	natural in / natural out]	natural or digit-reversed data order (?)
			BRAM budget	1000		maximum # of BRAMs to utilize (-1 for no limit) (?)
Quantu chiral -	at .		Generate Verilog	Reset		
@ www.spiral.n	et					

Possible Approach:

Capturing algorithm knowledge: *Domain-specific languages (DSLs)*

Structural optimization: *Rewriting systems*

High performance code style: *Compiler*

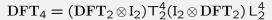
Decision making for choices: *Machine learning*

Organization

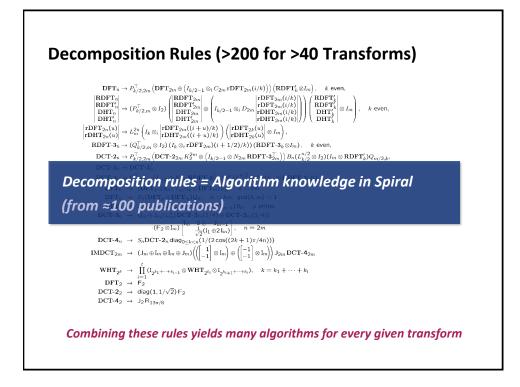
- Spiral: Basic system
- Vectorization
- General input size
- Results
- Final remarks



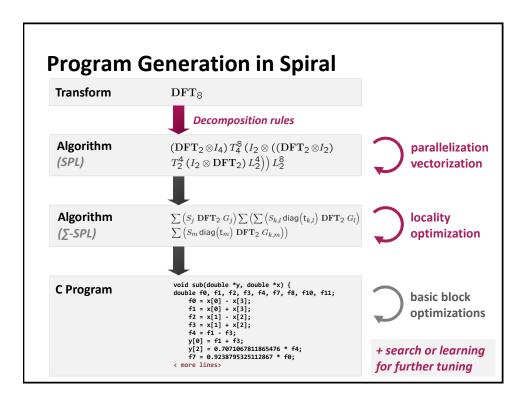
Representation using matrix algebra



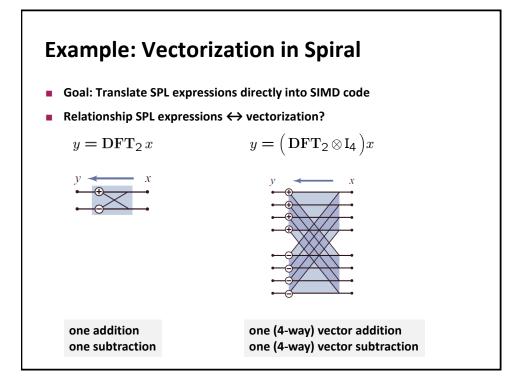
- SPL (Signal processing language): Mathematical, declarative, point-free
- Divide-and-conquer algorithms = breakdown rules in SPL

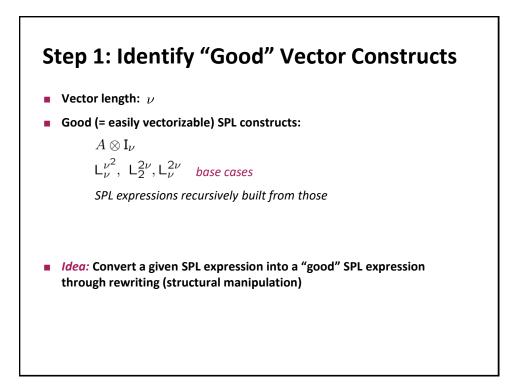


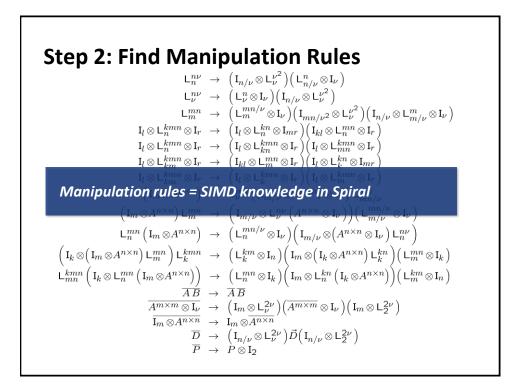
SPL S	Pseudo code for $y = Sx$	
$A_n B_n$	<code for:="" t="Bx"> <code for:="" y="At"></code></code>	Ги
$I_m \otimes A_n$	<pre>for (i=0; i<m; <code="" for:="" i++)="" y[i*n:1:i*n+n-1]="A(x[i*n:1:i*n+n-1])"></m;></pre>	$I_m \otimes A_n = \begin{vmatrix} A_n & & \\ & \ddots & \\ & & A_n \end{vmatrix}$
$A_m \otimes I_n$	<pre>for (i=0; i<n; <code="" for:="" i++)="" y[i:n:i+m*n-n]="A(x[i:n:i+m*n-n])"></n;></pre>	L
D_n	<pre>for (i=0; i<n; i++)="" y[i]="D[i]*x[i];</pre"></n;></pre>	
L_k^{km}	<pre>for (i=0; i<k; (j="0;" for="" i++)="" j++)="" j<m;="" y[i*m+j]="x[j*k+i];</pre"></k;></pre>	
<i>F</i> ₂	y[0] = x[0] + x[1]; y[1] = x[0] - x[1];	

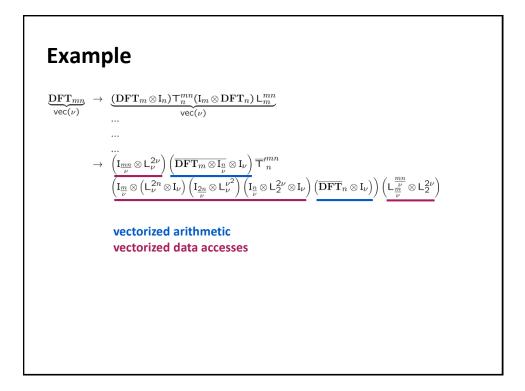


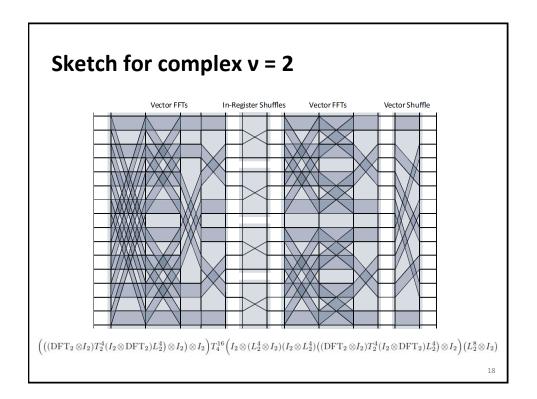


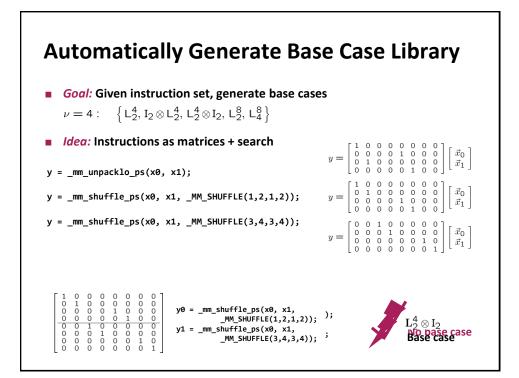


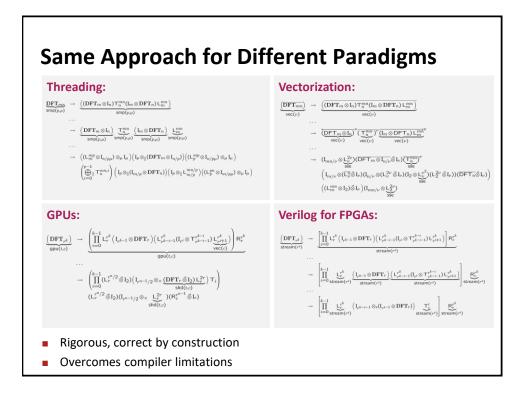








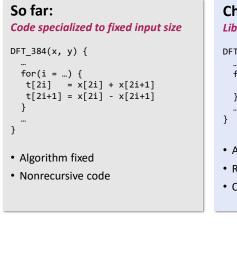






- Results
- Final remarks

Challenge: General Size Libraries

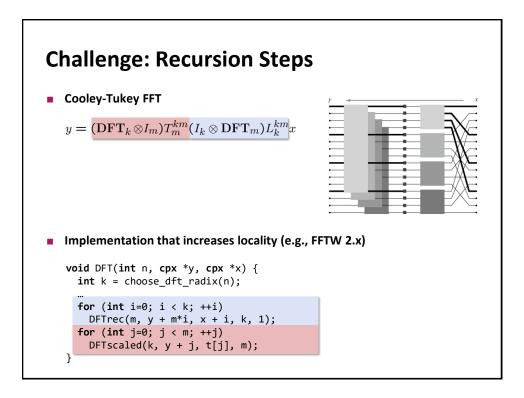


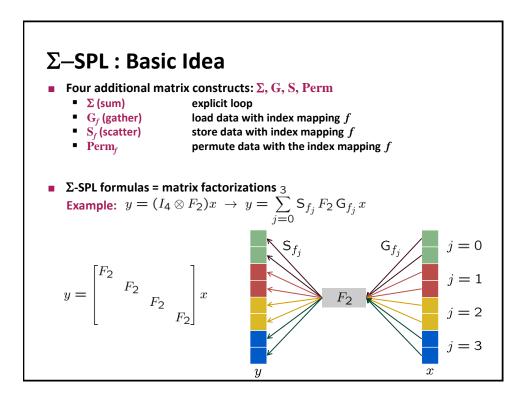
```
Challenge:
Library for general input size
```

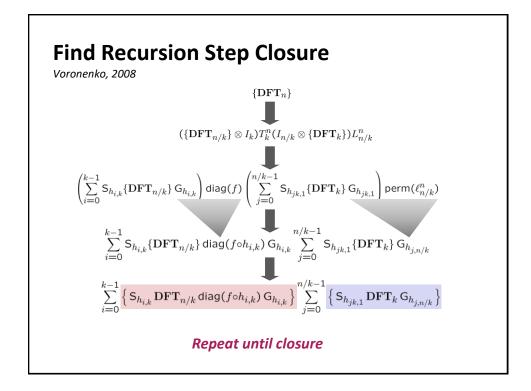
```
for(i = ...) {
    DFT_strided(m, x+mi, y+i, 1, k)
}
...
```

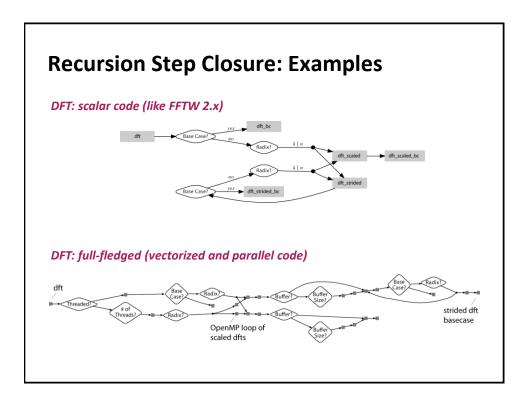
• Algorithm cannot be fixed

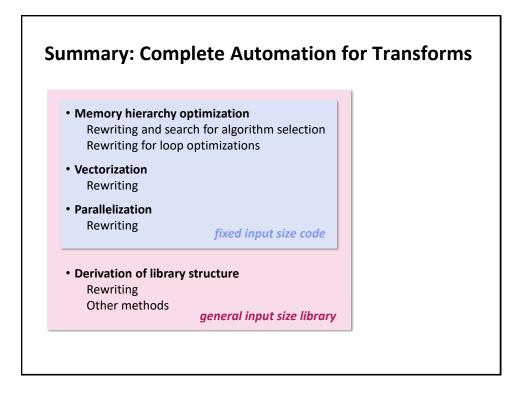
- Recursive code
- Creates many challenges

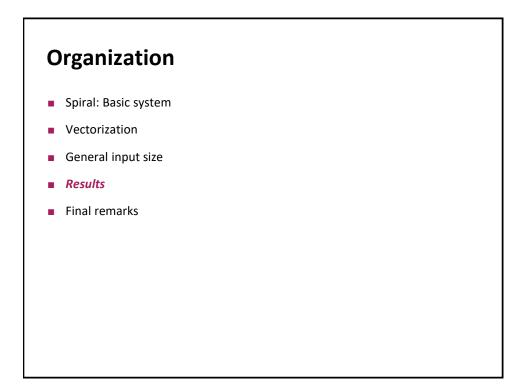


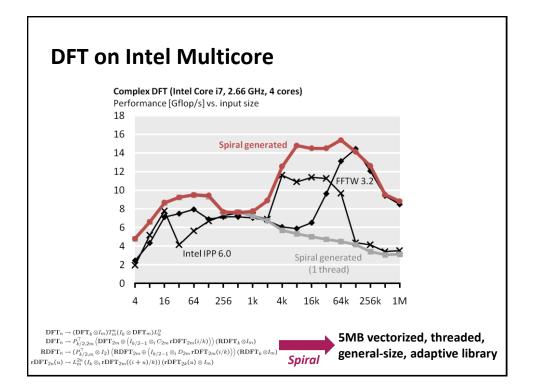


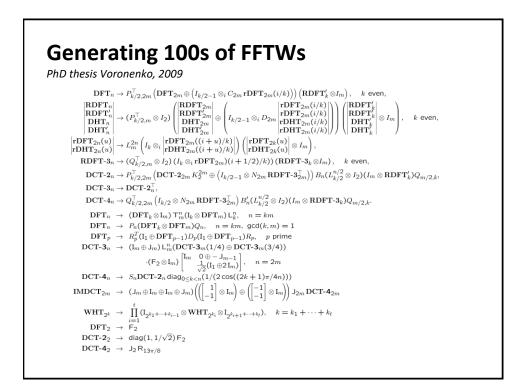








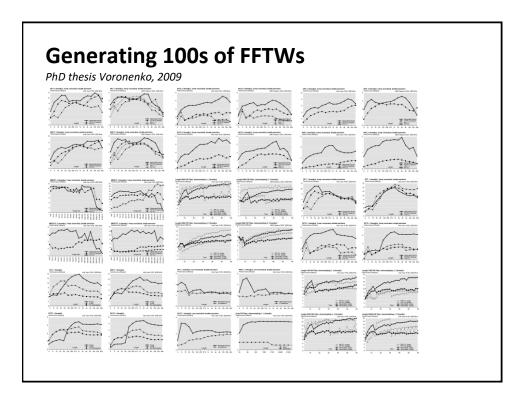


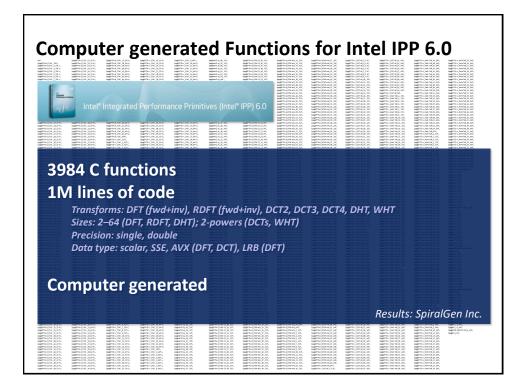


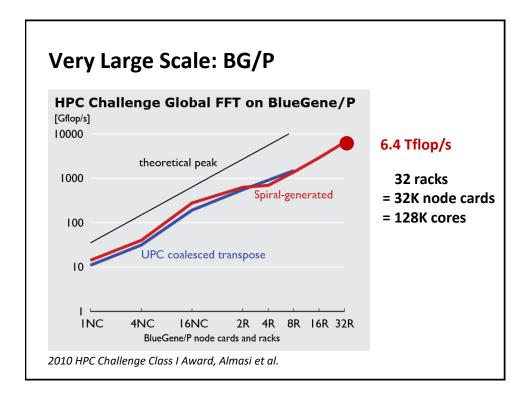
Generating 100s of FFTWs

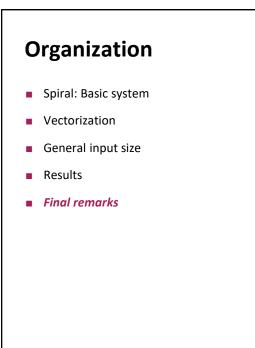
PhD thesis Voronenko, 2009

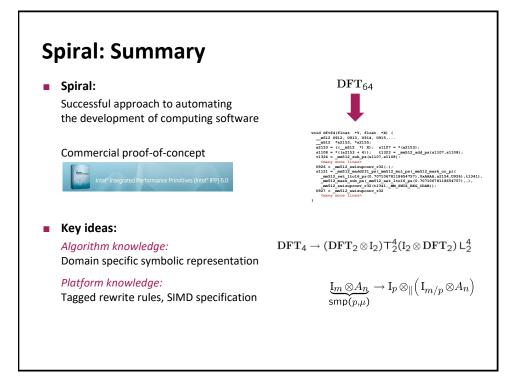
	Code size				
Transform	non-parallelized	parallelized			
no vectorization					
DFT	13.1 KLOC / 0.59 MB	10.3 KLOC / 0.45 MB			
RDFT	8.5 KLOC / 0.36 MB	8.8 KLOC / 0.39 MB			
DHT	9.1 KLOC / 0.40 MB	9.4 KLOC / 0.39 MB			
DCT-2	12.0 KLOC / 0.55 MB	12.4 KLOC / 0.57 MB			
DCT-3	12.0 KLOC / 0.56 MB	12.3 KLOC / 0.59 MB			
DCT-4	6.8 KLOC / 0.33 MB	7.1 KLOC / 0.35 MB			
WHT	5.6 KLOC / 0.21 MB				
2-way vectorization					
DFT	14.8 KLOC / 0.73 MB	15.0 KLOC / 0.74 MB			
RDFT	15.6 KLOC / 0.76 MB	16.0 KLOC / 0.81 MB			
scaled RDFT	16.0 KLOC / 0.78 MB				
DHT	16.9 KLOC / 0.83 MB	17.2 KLOC / 0.87 MB			
DCT-2	20.7 KLOC / 1.10 MB	21.0 KLOC / 1.09 MB			
DCT-3	27.9 KLOC / 1.56 MB	28.2 KLOC / 1.59 MB			
DCT-4	7.8 KLOC / 0.47 MB	8.1 KLOC / 0.50 MB			
WHT	6.9 KLOC / 0.32 MB	5.8 KLOC / 0.26 MB			
FIR Filter	167 KLOC / 7.75 MB	120 KLOC / 5.12 MB			
Downsampled FIR Filter	100 KLOC / 4.2 MB	68 KLOC / 2.76 MB			
4-way vectorization					
DFT	17.9 KLOC / 1.09 MB	18.2 KLOC / 1.11 MB			
RDFT	16.2 KLOC / 0.86 MB	16.5 KLOC / 0.91 MB			
scaled RDFT	16.5 KLOC / 0.88 MB				
DHT	17.9 KLOC / 1.02 MB	18.3 KLOC / 1.04 MB			
DCT-2	23.3 KLOC / 1.50 MB	23.6 KLOC / 1.53 MB			
DCT-3	32.0 KLOC / 2.17 MB	32.3 KLOC / 2.20 MB			
DCT-4	8.3 KLOC / 0.63 MB	8.6 KLOC / 0.66 MB			
WHT	8.5 KLOC / 0.53 MB	6.9 KLOC / 0.4 MB			
2D DFT	20.6 KLOC / 1.32 MB	20.8 KLOC / 1.33 MB			
2D DCT-2	27.0 KLOC / 2.1 MB	27.2 KLOC / 2.11 MB			
FIR Filter	109 KLOC / 5.69 MB	74 KLOC / 3.44 MB			
Downsampled FIR Filter	151 KLOC / 7.7 MB	92 KLOC / 4.61 MB			



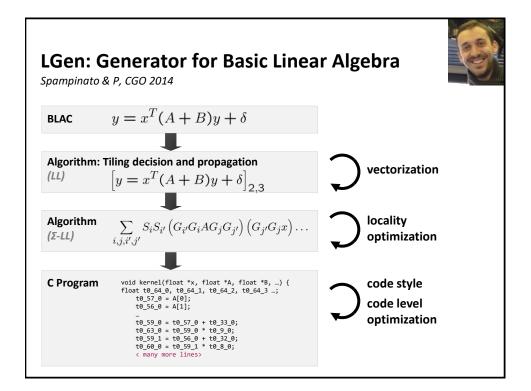


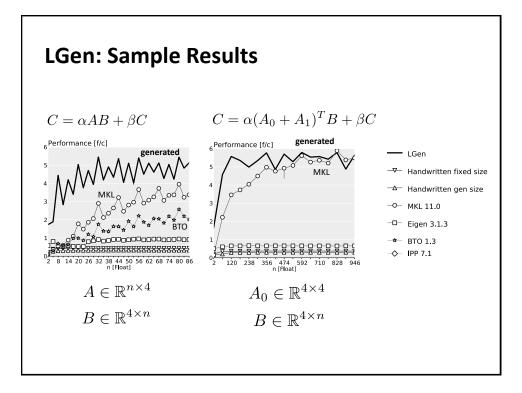


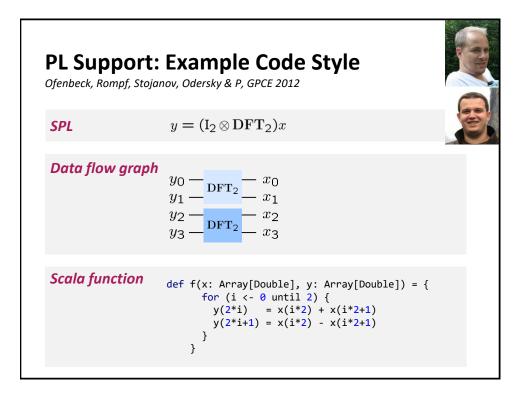


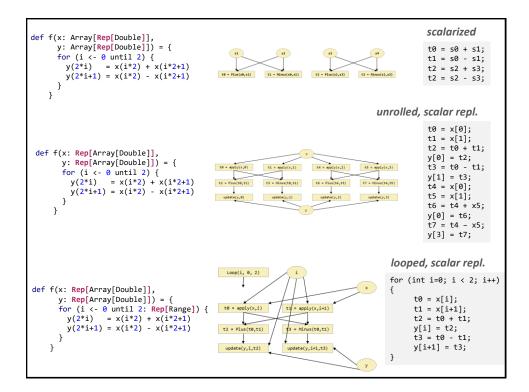


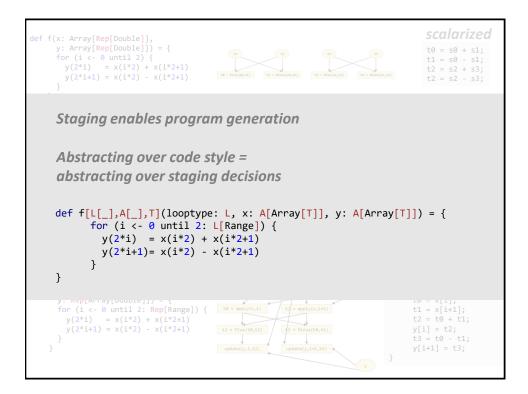


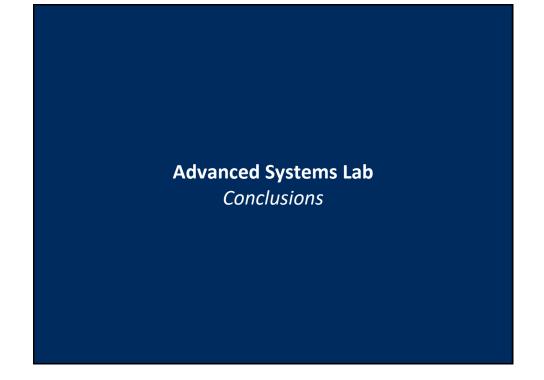


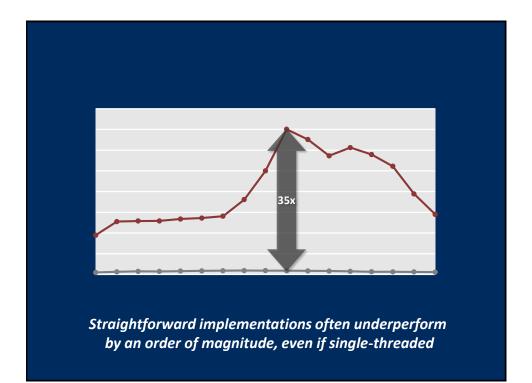


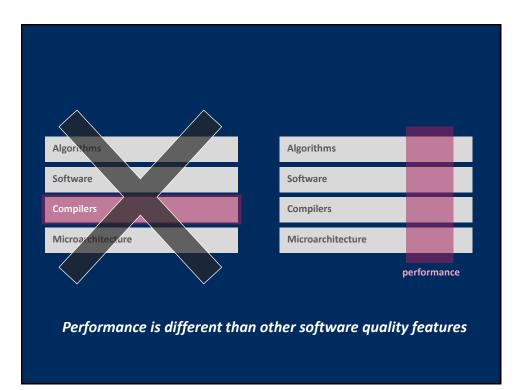












Research Questions

- How to port performance?
- How to automate the production of fastest numerical code?
 - Domain-specific languages
 - Rewriting
 - Compilers
 - Machine Learning
- What program language features help with program generation?
- What environment should be used to build generators?
- How to represent mathematical functionality?
- How to formalize the mapping to fast code?
- How to handle various forms of parallelism?
- How to integrate into standard work flows?